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DATE: October 9, 2003

PTO IDENTIFIER: Application Number 09/412,328
Patent Number

Inventor: Yuichi SATO

OFFICIAL

MESSAGE TO: Douglas A. Wille, GAU 2814, U.S. Patent and Trademark Office

FAX NUMBER: (703) 872-9318

FROM: MORRISON & FOERSTER LLP

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Attorney Dkt. #: 204552016500

PAGES (Including Cover Sheet): 1

CONTENTS: COMMUNICATION; and
Copy of Supplemental Amendment filed 7/30/03, which was not entered due to PTO error.

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PTO/SB/97 (12-97)

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COMMUNICATION; and
Copy of Supplemental Amendment filed 7/30/03.

Application No.: 09/412,328

1

Docket No.: 204552016500

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Yuichi SATO

Application No.: 09/412,328

Group Art Unit: 2814

Filed: October 5, 1999

Examiner: D. Wille

For: STATIC RANDOM ACCESS MEMORY
AND SEMICONDUCTOR DEVICE USING
MOS TRANSISTORS HAVING CHANNEL
REGION ELECTRICALLY CONNECTED
WITH GATE

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COMMUNICATION

Commissioner for Patents
P.O. Box 1450
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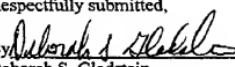
OFFICIAL

Dear Sir:

In accordance with the Examiner's request, Applicants forward a copy of the Supplemental Amendment filed on July 30, 2003 along with the and stamped postcard receipt. Applicants respectfully request that this Supplemental Amendment be entered in this application and that the Office Action dated September 10, 2003 be withdrawn so that the Supplemental Amendment may be considered.

Dated: October 9, 2003

Respectfully submitted,

By 
Deborah S. Gladstein
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Inventor: Yuichi SATO

Atty Docket No.: 204552016500

Application No.: 09/412,328

Filing Date: October 5, 1999

Title: STATIC RANDOM ACCESS MEMORY AND SEMICONDUCTOR DEVICE
USING MOS TRANSISTORS HAVING CHANNEL REGION ELECTRICALLY
CONNECTED WITH GATE

Documents Filed:

Supplemental Amendment Under 37 CFR 1.111 (4 pages)



Via: PTO Daily Run (With Certificate)

Date: July 30, 2003

Sender's Initials: DSG2/ntd2

I hereby certify that this correspondence is being hand delivered to: Commissioner for Patents, 2011 South Clark Place, Room 1803, Crystal Plaza 2, Arlington, Virginia, 22202, on the date shown below.	
Dated: <u>7/3/03</u>	Signature: <u>Nancy Delaney</u> (Nancy Delaney)

Docket No.: 204552016500
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Yuichi SATO

Application No.: 09/412,328

Group Art Unit: 2814

Filed: October 5, 1999

Examiner: D. Wille

For: STATIC RANDOM ACCESS MEMORY AND
SEMICONDUCTOR DEVICE USING MOS
TRANSISTORS HAVING CHANNEL REGION
ELECTRICALLY CONNECTED WITH GATE

SUPPLEMENTAL AMENDMENT UNDER 37 CFR 1.111

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action dated April 3, 2003 and supplemental to the Amendment Under 37 CFR 1.111 filed July 3, 2003, please amend the above-identified U.S. patent application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 4 of this paper.